

Khaled M. Elleithy, Ph.D.
Fellow, African Academy of Sciences
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Education

- Ph.D., Computer Science, the Center for Advanced Computer Studies (CACs), University of Louisiana at Lafayette, USA, May 1990.
- M.S., Computer Science, the Center for Advanced Computer Studies (CACs), University of Louisiana at Lafayette, USA, May 1988.
- M.S., Computer Science and Automatic Control, Computer Science and Automatic Control Dept., Faculty of Engineering, University of Alexandria, Egypt, July 1986.
- B.S., Computer Science and Automatic Control, Computer Science and Automatic Control Dept., Faculty of Engineering, University of Alexandria, Egypt, May 1983.

Ph.D. Thesis

“A Formal Framework for High Level Synthesis of Digital Designs,” M. A. Bayoumi (Chairman), The Center for Advanced Computer Studies, University of southwestern Louisiana, May 1990.

MS Thesis

“Multi-access Protocols in Local Area Networks,” M. S. Selim (Chairman), Computer Science and Automatic Control, Alexandria University, Alexandria, Egypt, July 1986.

Research Interests

Wireless Sensor Networks, wireless communications, and information security
Quantum Computing
Formal approaches for design and verification
Computer Architecture and Computer Arithmetic

Current Administrative Positions

Associate Vice President for Graduate Studies and Research, University of Bridgeport, Bridgeport, Connecticut, USA

Associate Dean for Graduate Programs, School of Engineering, University of Bridgeport, Bridgeport, Connecticut, USA

MS Online Program Advisor, of Bridgeport, *Bridgeport, Connecticut, USA*

- *Management of all aspects of the Distance Learning program in Computer Science*

Ph.D. Program Director, University of Bridgeport, *Bridgeport, Connecticut, USA*

- *Management of all aspects of the Ph.D. program in Computer Science and Engineering*

Current Academic Positions

Professor, Computer Science and Engineering Department, University of Bridgeport, Bridgeport, Connecticut, USA

Professor, Electrical Engineering Department, University of Bridgeport, Bridgeport, Connecticut, USA

Director, Mobile and Wireless Communications Laboratory, University of Bridgeport, Bridgeport, Connecticut, USA

Employment History

Associate Vice President for Graduate Studies and Research, University of Bridgeport, *Bridgeport, Connecticut, USA* (April 2014 – current)

Associate Dean for Graduate Programs, School of Engineering, University of Bridgeport, *Bridgeport, Connecticut, USA* (March 2006 – April 2014)

Professor, *Computer Science and Engineering Department, University of Bridgeport, Bridgeport, Connecticut, USA* (May 2003 - Now)

Digital logic	(CpE 210)	sophomore
Embedded System Design	(CpE 387)	Junior
Data Communications/Networks	(CpE 471)	Senior/Graduate
Database Design	(CS 450)	Senior/Graduate
Performance Evaluation of Systems	(CpE/CS 560)	Graduate
Network Security	(CpE/CS 561)	Graduate
Mobile Communications	(CPE481)	Senior/Graduate
Network Administration	(CPE 482)	Senior/Graduate
Wireless Sensor Networks	(CPSE 691)	Graduate
Quantum Computing	(CPEG 606)	Graduate

Adjunct Instructor, Computer Science Department, Sacred Heart University, Fairfield, Connecticut.

Network Security	(CS 622)	Graduate
Advanced Network Security	(CS 623)	Graduate

Online Instructor, Graduate InfoSec Program, Computer Science department, James Madison, MSC 4103, Harrisonburg, VA 22807 (Summer 2002, 2003)

Associate Professor, *Computer Science and Engineering Department, University of Bridgeport, Bridgeport, Connecticut, USA* (January 2000 – May 2003)

Associate Professor, Computer Engineering Department, King Fahd University of Petroleum and Minerals, Dhahran, Saudi Arabia. (April 95-Dec 99)

Assistant Professor, Computer Engineering Department, King Fahd University of Petroleum and Minerals, Dhahran, Saudi Arabia. (Sep90-April 95)

Undergraduate

Logic Design I	Sophomore
Computer Architecture I	Junior
Logic Design II	Junior
Data Communications for Business	Junior
Data Communications	Junior
Personal Computers	Senior
Computer Architecture II	Senior
Computer Networks	Senior
Bit slice microprocessor Design	Senior
VLSI RISC Architectures	Senior

Graduate

Architecture and Design of Computer Systems
Advanced Computer Architecture
Design of Arithmetic Units
Foundations of Hardware Design Verification.
Performance Evaluation of Computer Systems

Research Associate, The Center for Advanced Computer Studies, University of South Western Louisiana, Lafayette, USA. (May90-Sep90)

Research Assistant, The Center for Advanced Computer Studies, University of South Western Louisiana, Lafayette, USA. (May88-May90)

Teaching Assistant, Electrical and Computer Engineering Department, University of South Western Louisiana, Lafayette, LA, USA. Different Dept. Courses (Lab instructing, Grading) - 20 hours/week- (Aug86-May88)

Tutor, The International Student Office, University of South Western Louisiana, Lafayette, USA. (Aug86-May90)

System Programmer, Gathwary computers, Alexandria, Egypt. Participation in the design and implementation of different applications. Aug85-Aug86)

Lecturer, Air Defense College, Alex., Egypt. Digital systems and microprocessor courses and labs. -10 hours/week- (Apr85-Aug86)

System Programmer, Air Defense College, Alexandria, Egypt. Participation in the design and implementation of different data-base applications in the period (Jan84-Apr85)

Lecturer, Computer Sc. Dept., Alex. University, Egypt. Preparing, consulting, grading, and lab instructing for different department courses. -14 hours/week- (Oct83-Aug86)

Lecturer, Arab Maritime Transport Academy, Alex., Egypt. Programming Languages courses and Digital lab. -10 hours/week- (Aug83-May85)

Programmer, Computer Center, Achen University, Achen, West Germany. Design of a package for simulating time sharing systems (July 82-Sep82)

Programmer, Computer Sc. Dept., Faculty of Eng., Alex. University, Egypt. Participation in the design and implementation of many packages (Registration, planes reservation,). (July 80-Sep 80), (July 81-Sep81)

Research Grants

1. Principal Investigator, Cisco Networking Academy, Equipment Grant, \$16,000, Funded, November 2014.
2. Co-Investigator, Advanced Technology in Support of Hybrid Projectiles, FY 2010 Defense Appropriation Request, \$2.4 million; 1/1/2010 - 7/1/2012.

3. Co-Investigator, Hybrid Projectile Vision System, Imperial Machine and Tool Company, \$30,000; April 2010 - July 2010.
4. Co-Investigator, Development of a travel grant, U.S. Army Armament, Research, Development & Engineering Center (ARDEC), \$5,000, Funded. [1/2010]
5. Co-Investigator, Hybrid Projectile Design, U.S. Army Armament, Research, Development & Engineering Center (ARDEC), \$57,688, Funded. [8/1/2008 - 5/31/2009]
6. Principal Investigator, Implementation and Performance Evaluation of Overlay End System Multicast (ESM) for stable and Fast Streaming of Multimedia Applications, University of Bridgeport Seed Money Grants, \$10,000, Fall 2009, approved for funding.
7. Principal Investigator, Mobile Content Manager, United Nations Development Program (UNDP), Transfer of Knowledge through Expatriate Nationals (TOTKEN) Fund, \$3,500, June 2001, Funded.
8. Co-Investigator, Security Implementation and Evaluation of Aramco Enterprise Network, Saudi Armco, SR 200,000, May 1999- May 2000, Funded.
9. Principle Investigator, A Formal Methodology for Parallel VLSI Algorithm Design, Jan. 1993- Dec. 1995. King Abdul Aziz City of Science and Technology (KACST), SR 293,000, Funded.

Ph.D. Dissertation Supervision

1. Reem Mahjoob, Efficient Actor Recovery Paradigm for Wireless Sensor and Actor Networks, December 2017.
2. Abdulbast A. Abushgra, A New QKD Protocol Based Upon Authentication By EPR Entanglement State, December 2017.
3. Sumaya Abusaleh, A Novel Framework for Supervised Classification of Explosion Phenomena, December 2017.
4. Muneer Alshowkan, Deterministic and Efficient Three-Party Quantum Key Distribution, December 2017.
5. Wafa Elmannai, A Highly Accurate and Reliable Data Fusion Framework for Guiding Visually Impaired, December 2017.
6. Ramadhan Mstafa, Efficient and Robust Video Steganography Algorithms for Secure Data Communication, May 2017.
7. Adwan Alanazi, An Optimized Hidden Node Handling Approach for Improving the Coverage and Network Efficiency in Wireless Multimedia Sensor Networks, May 2017.
8. Abrar Alajlan, A Highly Reliable, Low Power Consumption, Low-Cost Multisensory Based System for Autonomous Navigational Mobile Robot, December 2016.
9. Marwah Almasri, Multi Sensor Fusion Based Framework for Efficient Mobile Robot Collision Avoidance and Path Following System, December 2016.
10. Bandar Alotaibi, Empirical Techniques to Detect Rogue Wireless Devices, December 2016.
11. Naser Alajmi, A Multi-Layer Approach For Detection of Selective Forwarding Attacks In Wireless Sensor Networks, December 2016.
12. Samir Hamada, Innovative Techniques for the Implementation Of Adaptive Mobile Learning Using The Semantic Web, May 2016.
13. Ibrahim Alkore Alshalabi, An Automated Adaptive Mobile Learning System Using Optimal Shortest Path Algorithms, May 2016.
14. Ammar Odeh, Robust Text Steganography Algorithms for Secure Data Communications, May 2015.
15. Abdul Razaque, Modular Energy Efficient Protocols for Lower Layers of Wireless Sensor Networks, May 2015
16. Laiali Almazaydeh, An Interactive, Real-time, High Precision And Portable Monitoring System of Obstructive Sleep Arena, August 2013.
17. Ali Elrashedi, A new Analytical Performance Model for Cylindrical Microstrip Printed Antenna, August 2012.
18. Mohammed A. Abuhelaleh, SOOAWSN: A secure object oriented architecture for wireless sensor networks, May 2011.
19. Syed Rizvi, A Generic Optimized Time Management Algorithms (OTMA) Framework for Simulating Large-Scale Overlay Networks, December 2010.

MS Thesis Supervision (Partial List)

- Topological Design of Enterprise Networks
- A Hardware Model of An expandable RSA Cryptographic System
- Evaluation and Comparison of Internet Firewalls
- Designing a Self-Timed ALU
- Organization of Parallel Memories
- Design of a Formal Specification Language for Parallel VLSI Algorithms
- Design and Modeling of a Real Time RISC Processor
- Backend Design of a Formal High Level Synthesis System
- A BIST for a Histogrammer Memory Chip

Senior Project Supervision (Partial list)

- Design of Residue Number System Arithmetic Processors.
- Performance Study of High Speed Adders
- Implementation of an Optimal Algorithm for RNS Arithmetic Processor Design
- Performance Study of RISC Processors
- Formal Hardware Verification
- A MIPS-X RISC Processor Emulator.
- Parallelizing Sorting Algorithms using OCCAM and Transputer
- An RNS Processor Emulator using Occam and Transputer.
- Hardware Verification Using an Expert System Shell
- Interfacing D/A and D/A Converters to a Transputer Based Machine
- A Transputer Based Sonar Range Finding
- Implementation of a new data link layer Protocol
- Analysis and study of Routing Protocols
- Design and implementation of Academic department database
- Translation of Web page to Arabic language

Honors

1. Recipient of the 2015 Connecticut Quality Improvement Award (CQIA) Gold Innovation Prize.
2. Distinguished Professor of the Year, University of Bridgeport, academic year 2006-2007.
3. Nominated for University of Bridgeport Distinguished Professor for academic years 2004 and 2005. Prof. Elleithy was one of two finalists.
4. Nominated for King Fahd University of Petroleum and Minerals for Best Teacher Award.
5. Distinguished Annual evaluation by KFUPM for 10 years in research, teaching and community services.
6. Honored at the “Academic Honors Day” of the University of South Western Louisiana for superior scholastic achievement, April 1988.
7. Certificate of merit, Air Defense College, Alexandria, Egypt, April 1985.
8. Badge of honor and Decoration, Graduation with the highest grade “Distinction with Degree of Honor” Egyptian Engineering Syndicate, June 1983.
9. Badge of honor and Decoration, Model student of Alexandria University for academic year 81/82, Alexandria University, Egypt, April 19882
10. Highest Scholarship, “Distinction” grade during all academic years, Alexandria University, Egypt, October 1978 - June 1983.

11. Badge of honor and Decoration, Scoring 96.25% in National High School Exam and nationally ranked fourth, Alexandria Governor, Egypt, June 1978.

Awards

1. Eugene Gerety and Khaled Elleithy, Best Student Poster Award, ASEE American Society Engineering Education North East Conference, University of Lowell, Lowell, MA, May 2017.
2. Wafa Elmannai and Khaled Elleithy, Best Graduate Student Poster Award, Connecticut Symposium on Microelectronics & Optoelectronics, University of Connecticut, CT, April 2017.
3. Reem Mahjoub and Khaled Elleithy, Best Graduate Student Poster Award, Connecticut Symposium on Microelectronics & Optoelectronics, University of Connecticut, CT, April 2017.
4. Eugene Gerety and Khaled Elleithy, Best Graduate Student Poster Award, Connecticut Symposium on Microelectronics & Optoelectronics, University of Connecticut, CT, April 2017.
5. Razan Abdulhammed and Khaled Elleithy, First Place Best Doctoral Student Poster Award, Faculty Research Day, Bridgeport, CT, March 2017.
6. Eugene Gerety and Khaled Elleithy, Second Place Best Doctoral Student Poster Award, Faculty Research Day, Bridgeport, CT, March 2017.
7. Naser M. Alajmi and Khaled Elleithy, Best Paper in the Technology Track, Best paper in Technology track, IEEE Long Island Systems, Applications and Technology Conference, May 2016.
8. Zakareya Lasefr and Khaled Elleithy, First Place, Student Poster Award for Doctoral Category, Faculty Research Day, University of Bridgeport, Bridgeport, CT, 2016.
9. Marwah Almasri and Khaled Elleithy, Student Poster Award for Doctoral Category, Faculty Research Day, University of Bridgeport, Bridgeport, CT, 2016.
10. Abrar Alajlan, Marwah Almasri and Khaled Elleithy, Fourth place Poster award, ASEE Conference, Rhode Island, 2016.
11. Zakareya Lasefr and Khaled Elleithy, Honorable mention, ASEE Conference, Rhode Island, 2016.
12. Wafa Elmannai: Advisor - Khaled Elleithy, UPE \$2500 Scholarship, 2015.
13. Abrar Alajlan, Marwah Almasri and Khaled Elleithy, Best paper in Applications track, IEEE Long Island Systems, Applications and Technology Conference, 2015.
14. Wafa Elmannai: Advisor - Khaled Elleithy, Highest Standard Oil Academic Accomplishments \$1,000 Award, May 2015.
15. Abdulbast Abushgra and Khaled Elleithy, Best Student Poster Award for Doctoral Category, Faculty Research Day, University of Bridgeport, Bridgeport, CT, 2015.
16. Wafa Elmannai: Advisor - Khaled Elleithy, \$20,000 award, Association of University Women International Fellowship for 2015-16, 2015.
17. Wafa Elmannai: Advisor - Khaled Elleithy, Recipient of the Collegian Innovation & Leadership Award, Connecticut Technology Council 2015 Women of Innovation Awards, 2015.
18. Mostafa Ramadhan and Khaled Elleithy, Fourth place honorable mention Poster award, ASEE Conference, Boston, 2015.
19. Wafa Elmannai and Khaled Elleithy, Third Place, Student Poster Award for Doctoral Category, Faculty Research Day, University of Bridgeport, Bridgeport, CT, 2015.
20. Ammar Odeh: Advisor: Khaled Elleithy, UPE \$750 Scholarship, 2014.
21. Muneer Alshowkan and Khaled Elleithy, Second Place, Student Poster Award for Doctoral Category, Faculty Research Day, University of Bridgeport, Bridgeport, CT, 2014.
22. Wafa Elmannai and Khaled Elleithy, Best Professional Paper Award, ASEE Conference, University of Bridgeport, Bridgeport, CT, 2014.
23. Tamer Youssef and Khaled Elleithy, Second Place Student Poster Award, ASEE Conference, University of Bridgeport, Bridgeport, CT, 2014.

24. Marwah Almasri: Advisor - Khaled Elleithy, UPE \$750 Scholarship, 2013.
25. Laiali Almazaydeh and Khaled Elleithy, Best Graduate Paper Award, ASEE Conference, Norwich University Northfield, VT, 2013.
26. Sumaya Abusaleh and Khaled Elleithy, Fourth place honorable mention Poster award, Faculty Research Day, Bridgeport, CT, 2012.
27. Wafa Elmannai: Advisor - Khaled Elleithy, UPE \$1250 Scholarship, 2012.
28. Laiali Almazaydeh and Khaled Elleithy, Fourth place honorable mention Poster award, ASEE Conference, University of Massachusetts, Lowell, MA, 2012.
29. Laiali Almazaydeh: Advisor - Khaled Elleithy, UPE \$1,500 Scholarship, 2011.
30. Eugene Gerety and Khaled Elleithy, Second Place, Student Poster Award, ASEE Conference, University of Bridgeport, Bridgeport, CT, 2011.
31. Eugene Gerety and Khaled Elleithy, Best Student Poster Award, Faculty Research Day, Bridgeport, CT, 2011.
32. Syed Rizvi and Khaled Elleithy, Best Poster Award, ASEE Conference, University of Bridgeport, Bridgeport, CT, 2009.
33. Syed Rizvi and Khaled Elleithy, Best Paper Award, 13th IEEE Symposium on Computers and Communications (ISCC'08), Marrakech, Morocco, 2008.

Professional Societies

- Senior Member of the IEEE computer society since 2013.
- Member of the Association of Computing Machinery (ACM) since 1990.
- Member of ACM SIGARCH (Special Interest Group on Computer Architecture) since 1990.
- Member of the honor society of Phi Kappa Phi University of South Western Louisiana Chapter since April 1989.
- Member of Circuits & Systems society since 1988.
- Member of the IEEE computer society since 1988.
- Member of the Egyptian Engineering Syndicate since June 1983

Professional Activities

Conferences Chairman

- Chairman, Annual IEEE Connecticut Conference on Industrial Electronics, Technology & Automation, Bridgeport, October 14 - 15, 2016
- Chairman, International Joint Conferences, on Computer, Information, and Systems Sciences, and Engineering, (CIS2E 14), December 2014, Virtual Conference.
- Chairman, International Joint Conferences, on Computer, Information, and Systems Sciences, and Engineering, (CIS2E 13), December 2013, Virtual Conference.
- Chairman, International Joint Conferences, on Computer, Information, and Systems Sciences, and Engineering, (CIS2E 12), December 2012, Virtual Conference.
- Chairman, International Joint Conferences, on Computer, Information, and Systems Sciences, and Engineering, (CIS2E 11), December 2011, Virtual Conference.
- Chairman, International Joint Conferences, on Computer, Information, and Systems Sciences, and Engineering, (CIS2E 10), December 2010, Virtual Conference.
- Chairman, International Joint Conferences, on Computer, Information, and Systems Sciences, and Engineering, (CIS2E 09), December 2009, Virtual Conference.
- Chairman, International Joint Conferences, on Computer, Information, and Systems Sciences, and Engineering, (CIS2E 08), December 2008, Virtual Conference.
- Chairman, International Joint Conferences, on Computer, Information, and Systems Sciences, and Engineering, (CIS2E 07), December 2007, Virtual Conference.
- Chairman, International Joint Conferences, on Computer, Information, and Systems Sciences, and Engineering, (CIS2E 06), December 2006, Virtual Conference.

- Chairman, International Joint Conferences, on Computer, Information, and Systems Sciences, and Engineering, (CIS2E 05), December 2005, Virtual Conference.
- Chairman, IEEE International Conference on Industrial Electronics, Technology & Automation, IETA 2001, 19-21 December 2001, Cairo – Egypt.

Membership in Technical Committees

1. Member, 2010 Program Committee of Workshop on System-on-Chip for Real-Time Applications, Canada, July, 2010.
2. Member, 2009 Program Committee of Workshop on System-on-Chip for Real-Time Applications, Canada, July, 2009.
3. Member, 2008 Program Committee of Workshop on System-on-Chip for Real-Time Applications, Canada, July, 2008.
4. Member, 2007 Program Committee of Workshop on System-on-Chip for Real-Time Applications, Canada, July, 2007.
5. Member, 2006 Program Committee of Workshop on System-on-Chip for Real-Time Applications, Canada, July, 2006.
6. Member, Program Committee of World Multiconference on Systemics, Cybernetics and Informatics (SCI 2006) Orlando, USA, July, 2006.
7. Member, 2005 Program Committee of Workshop on System-on-Chip for Real-Time Applications, Canada, July, 2005.
8. Member, Program Committee of World Multiconference on Systemics, Cybernetics and Informatics (SCI 2005) Orlando, USA, July, 2005.
9. Member, 2004 Program Committee of Workshop on System-on-Chip for Real-Time Applications, Canada, July, 2004.
10. Member, 2004 Program Committee of Workshop on System-on-Chip for Real-Time Applications, Canada, July, 2004. Member, World Multiconference on Systemics, Cybernetics and Informatics (SCI 2004) Orlando, USA, July, 2004.
11. Member, 2003 Program Committee of Workshop on System-on-Chip for Real-Time Applications, Canada, July, 2003.
12. Member, 2003 International Program Committee of the 15th International Conference on Parallel and Distributed Computing and Systems in Marina del Rey, CA, USA, November 3-5, 2003.
13. Member, 2003 Program Committee of Workshop on System-on-Chip for Real-Time Applications, Calgary, Canada, July, 2003.
14. Member, 7th World Multiconference on Systemics, Cybernetics and Informatics (SCI 2003) Orlando, USA, July, 2003.
15. Member, 2002 International Program Committee of the 14th International Conference on Parallel and Distributed Computing and Systems in Cambridge (USA), November 4-6, 2002.
16. Member, 6th World Multiconference on Systemics, Cybernetics and Informatics (SCI 2002) Orlando, USA, July, 2002.
17. Member, 2002 Program Committee of Workshop on System-on-Chip for Real-Time Applications, Banff, Canada, July 6-7, 2002
18. Member, International Program Committee of the 13th International Conference on Parallel and Distributed Computing and Systems (PDCS'01) in California (USA), August 21 - 24, 2001
19. Member, Technical committee of the 2001, Applied Informatics (AI) 2001, February 19-22, 2001 Innsbruck, Austria
20. Member, Program Technical Committee of the 2001 IEEE International Conference on Robotics and Automation in Seoul, Korea
21. Member, Program Technical Committee of the 2000 IEEE Workshop on SIGNAL PROCESSING SYSTEMS (SiPS) Design and Implementation, October 11-13, 2000, Lafayette, Louisiana.
22. Member, Program Technical Committee of 8th IEEE Great Lake Symposium on VLSI, GLS'98, Feb. 1998, Lafayette, Louisiana.
23. Member, Program Technical Committee of Conference on Parallel and Distributed Computing and Systems, PDCS 97, June 1997, Barcelona.
24. Member, Program Technical Committee of Eighth IASTED International Conference on Parallel and Distributed Conference, Oct. 1996, Chicago.
25. Member, Program Technical Committee of seventh International Conference on Microelectronics, Dec 1995, Malaysia.

26. Member, Program Technical Committee of 37th IEEE Midwest Symposium on Circuits and Systems, August 3-5, Lafayette, LA, USA, 1994.
27. Member, Program Technical Committee of sixth International Conference on Microelectronics, September 5 - 7, Istanbul, Turkey, 1994.
28. Member, Program Technical Committee of fifth International Conference on Microelectronics, Dec. 13-16, 1993, Dhahran, Saudi Arabia.

Membership in Standing Committees

Member, IASTED Standing Technical Committee on Parallel and Distributed Computing and Systems. The committee is responsible for the planning and organization of IASTED activities such as meetings and publications.

Member, American Biographical Institute Research Board of Advisors. The American Biographical Institute publishes the following biographical reference works since 1967:

The International Directory of Distinguished Leadership
The International Who's Who of Professional and Business Women
The Dictionary of Leading Americans and the World Who's Who of Women

Editorial

- Editor Board Member, Electronics, MDPI.
- Editor Board Member, Communications, MDPI.
- Editor, Special issue on Wireless LANs, Journal of Internet Technology, April 2003.
- Editor, Special issue on Web-based controlled devices, International Journal of Robotics and Automation, November 2002.
- Editor, Special issue on network security, Journal of Internet Technology, April 2002.

Published Work

Books

1. Khaled Elleithy and Tarek Sobh, 2016 Annual Connecticut Conference on Industrial Electronics, Technology and Automation (CT-IETA), IEEE Xplore, 2016, ISBN 978-3-319-06764-3
2. Tarek Sobh, and Khaled Elleithy, 2015 U.S. Higher Education Faculty Awards, Vol. 3, River Publishers Series, 2015, ISBN 9788793379022
3. Tarek Sobh, and Khaled Elleithy, 2015 U.S. Higher Education Faculty Awards, Vol. 2, River Publishers Series, 2015, ISBN 9788793379015
4. Tarek Sobh, and Khaled Elleithy, 2015 U.S. Higher Education Faculty Awards, Vol. 1, River Publishers Series, 2015, ISBN 9788793379008
5. Khaled Elleithy and Tarek Sobh, *New Trends in Networking, Computing, E-learning, Systems Sciences, and Engineering*, Springer 2015.
6. Tarek Sobh and Khaled Elleithy, *Innovations and Advances in Computing, Informatics, Systems Sciences, Networking and Engineering*, Springer, 2015.
7. Tarek Sobh and Khaled Elleithy, *Emerging Trends in Computing, Informatics, Systems Sciences, and Engineering*, Springer, 2013.
8. Khaled Elleithy and Tarek Sobh, *Innovations and Advances in Computer, Information, Systems Sciences, and Engineering*, Springer, 2013.
9. Tarek Sobh and Khaled Elleithy, *Innovations in Computing Sciences and Software Engineering*, Springer, November 2010.
10. Khaled Elleithy, Tarek Sobh, Magued Iskander, V Kapila, M.A. Karim, and Ausif Mahmood, *Technological Developments in Networking, Education and Automation*, July 2010.
11. Tarek Sobh, Khaled Elleithy, Ausif Mahmood, *Novel Algorithms and Techniques in Telecommunications and Networking*, Springer, March 2010.
12. Khaled Elleithy, *Advanced Techniques in Computing Sciences and Software Engineering*, Springer, March 2010.

13. Khaled Elleithy, *Innovations in Systems, Computing Sciences and Software Engineering*, Springer, August 2008.
14. Tarek Sobh, Khaled Elleithy, Ausif Mahmood and Mohamed Karim, *Novel Algorithms and Techniques in Telecommunications, Automation and Industrial Electronics*, Springer, August 2008.
15. Khaled Elleithy, *Advances and Innovations in Systems, Computing Sciences and Software Engineering*, Springer, June 2007.
16. Tarek Sobh, Khaled Elleithy, Ausif Mahmood and Mohamed Karim, *Innovative Algorithms and Techniques in Automation, Industrial Electronics and Telecommunications*, Springer, June 2007.
17. Khaled Elleithy, Tarek Sobh, Ausif Mahmood, Magued Iskander and Mohammad Karim, *Advances in Computer, Information, and Systems Sciences, and Engineering*, Springer, September 2006.
18. Tarek Sobh and Khaled Elleithy *Advances in Systems, Computing Sciences and Software Engineering: Proceedings of SCSS 2005*, Springer, September 2006.
19. Tarek Sobh, Khaled Elleithy and Aziza Zaki, *Proceedings of the International Conference on Industrial Electronics, Technology and Automation (IETA 2001)*, IEEE Industrial Electronics Society, December 2001.

Book Chapters

1. Razan Abdulhammed, Miad Faezipour and Khaled Elleithy, "Intrusion Detection System in Self-Organizing Networks: A Survey", accepted in CRC Series in Security, Privacy and Trust - Taylor & Francis, Intrusion Detection and Prevention for Mobile Ecosystems, Nov. 2016.
2. Islam Ashry, Ali Elrashidi Khaled Elleithy, "Variable Delay Optical Buffer Using Tunable Fiber Bragg Grating," *New Trends in Networking, Computing, Informatics, Systems Sciences, and Engineering*, Springer 2014.
3. Ali Elrashidi, Islam Ashry Khaled Elleithy Hassan Bajwa, "The Effect of Mutual Coupling on a Microstrip Printed Antenna Array Operates at 5 GHz Using Three Different Substrates," *New Trends in Networking, Computing, Informatics, Systems Sciences, and Engineering*, Springer 2014.
4. Ammar Odeh, Khaled Elleithy, Miad Faezipour Eman Abdelfattah, "Novel Steganography over HTML Code," *New Trends in Networking, Computing, Informatics, Systems Sciences, and Engineering*, Springer 2014.
5. Abdul Razaque and Khaled Elleithy, "Detection of Attacks for Restoring Privacy of Users to Improve Mobile Collaborative Learning (MCL) Over Heterogeneous Network," *Communications in Computer and Information Science*, Springer, Vol. 294, 2012, pp 201-216.
6. Abdul Razaque and Khaled Elleithy, "Ambient Intelligence Paradigm to support pedagogical activities over wireless sensor networks," *Ubiquitous Computing and Ambient Intelligence (UCAmI)*, Springer, 2012.
7. Abdul Razaque, Aziz Alotaibi, Khaled Elleithy "Location Based Overlapped Mobility Aware network Model," *Innovations and Advances in Computer, Information, Systems Sciences, and Engineering*, Springer, September 2012.
8. Syed S. Rizvi, Khaled Elleithy, and Aasia Riasat, "The Use of Orthogonal Frequency Code Division (OFCD) Multiplexing in Wireless Mesh Network (WMN)," *Fourth-Generation (4G) Wireless Networks: Applications and Innovations*, pp. 565-581, 2010.
9. Syed S. Rizvi, Majid A. Jafri, and Khaled Elleithy, "A Novel Optimization of the Distance Source Routing (DSR) Protocol for the Mobile Ad Hoc Networks (MANET)," *Novel Algorithms and Techniques in Telecommunications and Networking*, Springer, pp. 269 – 274, 2010.
10. Aasia Riasat, Syed S. Rizvi, Khaled M. Elleithy, Farheen Zehara, and Faraz Arain, "The Role of System Dynamics in Learning Environments," *Advances in Automation, Industrial Electronics and Telecommunications*, Springer, June 2008
11. Syed S. Rizvi, Khaled M. Elleithy, Khushboo Patel, and Chaitali Patel, "Use of Self-Adaptive Methodology in Wireless Sensor Networks for Reducing the Energy Consumption," *Advances in in Automation, Industrial Electronics and Telecommunications*, Springer, June 2008
12. Syed S. Rizvi, Aasia Riasat, Muhammad S. Rashid, and Khaled M. Elleithy, "Bandwidth Problem in High Performance Packet Switching Network," *Innovations in Systems, Computing Sciences and Software Engineering*, Springer, June 2008.
13. Ajay Shrestha, Khaled M. Elleithy, and Syed S. Rizvi, "Investigating the effects of Encoder Schemes, WFQ & SAD on VoIP QoS," *Advances in Automation, Industrial Electronics and Telecommunications*, Springer, June 2008.

14. Aasia Riasat, Syed S. Rizvi, Khaled M. Elleithy, Faraz Arain, Rizwan M. Qureshi, "Dynamic Semantics of the Web: Useful Toll for the New Generation Agent-Based Software," *Advances in Automation, Industrial Electronics and Telecommunications*, Springer, June 2008.
15. Khurram M. Rajput, Khaled M. Elleithy, and Syed S. Rizvi, "A Novel Approach for Creating Consistent Trust and Cooperation (CTC) among Mobile Nodes of Ad Hoc Network," *Advances in Automation, Industrial Electronics and Telecommunications*, Springer, June 2008.
16. Syed S. Rizvi, K. M. Elleithy, Aasia Riasat, "Minimizing the Null Message Exchange in Conservative Distributed Simulation," Book Chapter, *Innovative Algorithms and Techniques in Automation, Industrial Electronics and Telecommunications*, Springer, June 2007.
17. K. M. Elleithy and I. Rimawi, "Design, Analysis and Implementation of a Cyber Vote System." Book Chapter, *Advances in Computer, Information, and Systems Sciences, and Engineering: Proceedings of IETA 2005, TeNe 2005 and EIAE 2005*, Springer, September 2006.

Journal Publications

1. Sumaya Abusaleh, Ausif Mahmood, Khaled Elleithy, "The Art of Reading Explosion Phenomena: Science and Algorithms," Accepted to appear in IEEE Access Journal, 2018.
2. Wafa Elmannai and Khaled Elleithy, "A Highly Accurate and Reliable Data Fusion Framework for Guiding the Visually Impaired." Accepted to appear in IEEE Access Journal, 2018.
3. Muneer Alshowkan and Khaled M. Elleithy, "Deterministic and Efficient Quantum Key Distribution Using Entanglement Parity Bits and Ancillary Qubits, " IEEE Access Journal, Vol. 5, December 2017, pp. 25565 - 25575.
4. Abdulbast Abushgra and Khaled M. Elleithy, "A Shared Secret Key Initiated by EPR Authentication and Qubit Transmission Channels," IEEE Access Journal, Vol. 5, September 2017, pp. 17753 - 17763.
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8. "Steganography in Arabic Text Using Full Diacritics Text," CAINE-2012, 25th International Conference on Computer Applications in Industry and Engineering, New Orleans, Louisiana, November 14 – 16, 2012.
9. "Development of OSA Event Detection Using Threshold Based Automatic Classification," CAINE-2012, CAINE-2012, 25th International Conference on Computer Applications in Industry and Engineering, New Orleans, Louisiana, November 14 – 16, 2012.
10. "Optimized Algorithm for Fire Detection over WSN using Micaz Motes," CAINE-2012, 25th International Conference on Computer Applications in Industry and Engineering, New Orleans, Louisiana, November 14 – 16, 2012.
11. "Efficient and Optical Character Recognition Algorithm for Signature Recognition," CAINE-2012, 25th International Conference on Computer Applications in Industry and Engineering, New Orleans, Louisiana, November 14 – 16, 2012.
12. "Parallel Processing for Multi Face Detection and Recognition," CAINE-2012, 25th International Conference on Computer Applications in Industry and Engineering, New Orleans, Louisiana, November 14 – 16, 2012.
13. "Underwater Wireless Sensor Network Communication Using Electromagnetic Waves at Resonance Frequency 2.4 GHz," 15th Communications and Networking Simulation (CNS'12) Symposium, Orlando, FL, USA, March 26 - 29, 2012.
14. "A Use of Matrix with GVT Computation in Optimistic Time Warp Algorithm for Parallel Simulation, 15th Communications and Networking Simulation (CNS'12) Symposium, Orlando, FL, USA, March 26 - 29, 2012.
15. "Security in Wireless Sensor Networks: Key Intrusion Detection Module in SOOAWSN," 14th Communications and Networking Simulation (CNS'11) Symposium, Boston, April 4 – 7, 2011.
16. "A Generic Optimized Time Management Algorithms (OTMA), Framework for Simulating Large-Scale Overlay Networks," 14th Communications and Networking Simulation (CNS'11) Symposium, Boston, April 4 – 7, 2011.
17. "Transformation Matrix Algorithm for Reducing the Computational Complexity of Multiuser Receivers for DS-CDMA Wireless Systems," Wireless Telecommunication Symposium (WTS 2007), Pomona, California, April 26-28, 2007.
18. "Implementations of Location Awareness Technologies and their Applications" Wireless Telecommunication Symposium (WTS 2007), Pomona, California, April 26-28, 2007.
19. "Implementation and Comparison of Denial of Service Attack Techniques," the 8th World Multiconference on Systemics, Cybernetics and Informatics (SCI 2004), Orlando, USA, July 18-21, 2004.
20. "A Framework for Reverse Engineering VLSI Chips," IFAC Conference on Intelligent Control and Signal Processing, Portugal, April 2003.
21. "Support of a Banking System Using WAP," The 6th World Multiconference on systemics, cybernetics, and Informatics, Orlando, USA, July 14-18, 2002.
22. "Simulation and Improvement of IMT2000 Power Control," The 6th World Multiconference on systemics, cybernetics, and Informatics, Orlando, USA, July 14-18, 2002.
23. "E-commerce" Public Lecture organized by Royal Commission at Jubail, Saudi Arabia, Dec. 1999.
24. "Area Estimation for DSP Algorithms," IEEE Workshop on SiGNAL PROCESSING SYSTEMS (SiPS) Design and Implementation, Lafayette, Louisiana, October 11-13, 2000.
25. "E-commerce" Saudi Internet 98 Meeting, Riyadh, Dec. 5-7, 1999
26. "A Genetic Algorithm for Register Allocation" 9th great lake Symposium on VLSI, Michigan, March 1999.
27. "A Simulated Annealing Algorithm for Register Allocation," Fifth Saudi Engineering Conference, March 1999
28. "Net Security" Information Technology Center Internal Seminar for ITC Staff, Dhahran, Saudi Arabia, Nov. 1998.
29. "Internet/Intranet for Business" Dhahran 15th Annual Computer, Communication & Office Technology Exhibition, Oct. 17-22, 1998.
30. "HOOVER: Hardware Object-Oriented Verification," 8th Great Lake Symposium on VLSI, GLS'98, Lafayette, Louisiana, Feb. 1998.
31. "New Non-deterministic Approaches for Register Allocation," 4th IEEE International Conference on Electronics, Circuits, and Systems, Cairo, Dec. 1997.
32. "HOOVER: Hardware Object-Oriented Verification," 8th Great Lake Symposium on VLSI, GLS'98, Lafayette, Louisiana, Feb. 1998.

33. "Electronic Commerce," Annual Computer Exhibition, KFUPM, April 1997.
34. "Formal Hardware Verification," ICS/COE Fall Seminar Series, KFUPM, Dec. 1996.
35. "Choosing System Moduli in RNS," ICS/COE Fall Seminar Series, Nov. 1996.
36. "Choosing System Moduli for RNS Arithmetic Processors," 30th Annual Asilomar Conference on Signals, Systems, and Computers, Pacific Grove, California, Nov. 3-6, 1996.
37. "Synthesis of Digital Signal Processing Algorithms from Formal Descriptions," The Sixth International Conference on Signal Processing Applications and Technology, Boston, Oct. 24-26, 1995.
38. "An Event Logic Architecture for CIRCAL Algorithms," The Seventh IASTED International Conference on Parallel and Distributed Computing and Systems, Washington, D. C., Oct. 18-21, 1995.
39. "An Integer Programming Approach for Choosing the System Moduli for RNS Processor," Minisymposium on Optimization Theory and Applications, KFUPM, Dhahran, May 30, 1995.
40. "Formal Verification of DSP VLSI Architectures," International Conference on Electronics, Circuits and Systems, Cairo, Egypt, Dec. 19-22, 1994.
41. "Formal Environments for Parallel Hardware Description Languages," International Conference on Electronics, Circuits and Systems, Cairo, Egypt, Dec. 19-22, 1994.
42. "Formal Verification of DSP VLSI Architectures: A Tutorial" 37th Midwest Symposium on Circuits and Systems, Lafayette, Louisiana, August 3-5, 1994.
43. "A Rule-based Approach for High Speed Adders Design Verification," 37th Midwest Symposium on Circuits and Systems, Lafayette, Louisiana, August 3-5, 1994.
44. "A Transputer Based Sonar Range finding," Workshop on Optimization and Parallel Computation, Al-Ain, United Arab Emirates, May 8-11, 1994.
45. "A Characteristic Model for Formal Parallel Hardware Description Languages," Workshop on Optimization and Parallel Computation, Al-Ain, United Arab Emirates, May 8-11, 1994.
46. "Formal Hardware Verification of VLSI Architecture Current Status and Future Directions," Fifth International Conference on Microelectronics, Dhahran, Saudi Arabia, Dec. 1993.
47. "A Production Based System for Formal Verification of Digital Signal Processing Architectures," Twenty-Seventh Annual Asilomar Conference on Signals, Systems and Computers, Pacific Grove, California, Nov. 1993.
48. "PLORA: A Prolog and LISP oriented RISC Architecture," EinShams University, Cairo, April 1993.
49. "RISC Architectures," Information & Computer Science Department Spring Lecture Series, King Fahd University, May 1992.
50. "Foundations of Hardware Design Correctness," Computer Engineering Department, King Fahd University, December 1991.
51. "A Massively Parallel RNS Architecture," 25th Asilomar Conference on Circuit and Systems, Pacific Grove, California, Nov. 1991.
52. "Formal High Level Synthesis," Computer Engineering Department, King Fahd University, December 1990.
53. "Synthesizing DSP Architectures from Behavioral Specifications: A Formal Approach," 1990 IEEE International Symposium for Circuits and Systems, New Orleans, May 1990.
54. "Formalizing Behavioral Specifications Synthesis," Louisiana State University, Louisiana, April 1990.
55. "A Framework for High Level Synthesis of Digital Architectures from U-recursive Algorithms," ACM Eighteenth Annual Computer Science Conference, Washington D. C., Feb. 1990.
56. "Efficient Implementations of the Chinese Remainder Theorem," Computer Science and Automatic Control Department, Alexandria University, July 1989.
57. "A Systolic Machine for Relational Database and Hashing," Third Annual Parallel Processing Symposium, Los Angeles, Mar. 1989.
58. "Residue Arithmetic: Parallelism at the Algorithmic Level," The Center for Advanced Computer Studies, University of Southwestern Louisiana, November 1987.